



SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION  (PTO-1449)				ATTY. DOCKET NO. 057454-0964		SERIAL NO. 10/615,379	
				APPLICANT Hideto HIDAKA			
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U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code2 (if known)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
VTN		SCHEUERLEIN, Roy E. et al., "Shared Word Line DRAM Cell", IEEE Journal of Solid-State Circuits, Vol. 19, No. 5, October 1984, pp. 640-645					
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EXAMINER 				DATE CONSIDERED 8/8/05			

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